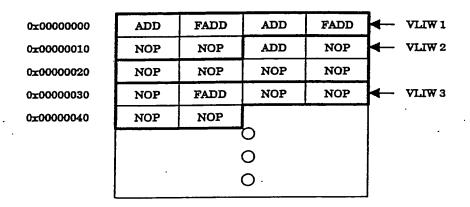


FIG. 2



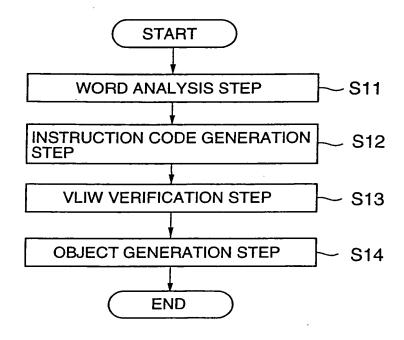
PRIOR ART

F16.3

	INSTRUCTION REGISTER	ION REG	ISTER					
TIME 1	ADD	ADD FADD	QQY	FADD	NOP	NOP ← VLIW 1	\	VLIW 1
TIME 2	ADD	NOP	NOP	NOP	NOP	NOP	\	← VLIW 2
TIME 3	NOP	FADD	HON	NOP	HON	NOP	\	→ VLIW 3

PRIOR ART

FIG.4



PRIOR ART _-

FIG.5

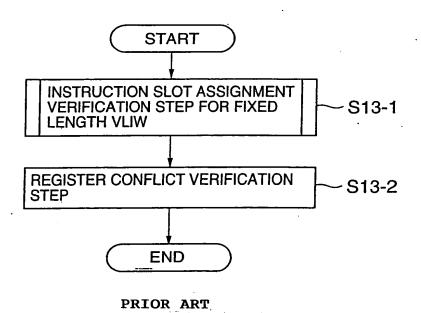
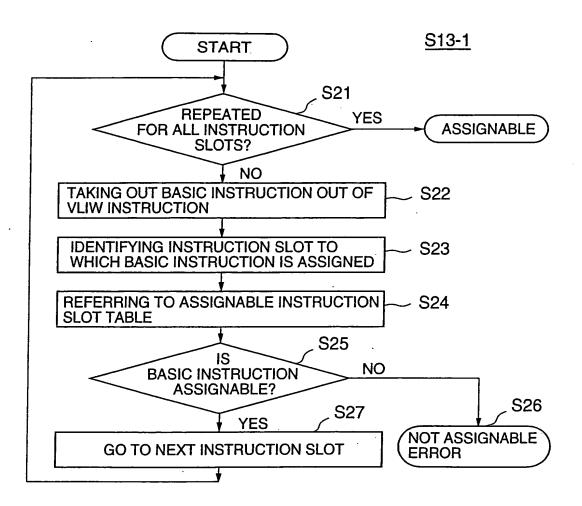


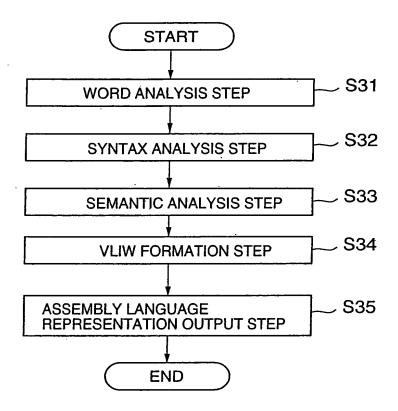
FIG.6



PRIOR ART

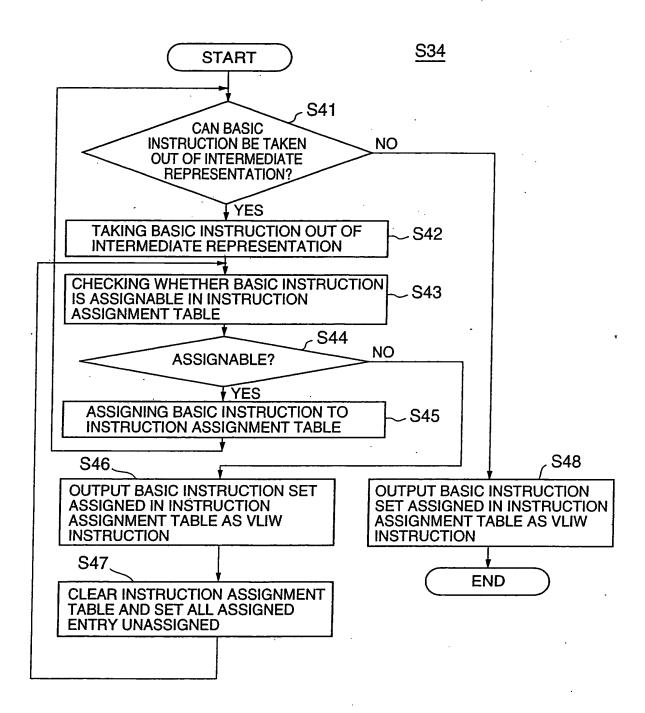
		BRA	FADD	ADD	INSTRUCTION	BASIC
: N	O : AS	1		0	SLOT 0	INSTRUCTION SLOT
— : NOT ASSIGNABLE	O: ASSIGNABLE		0	-	SLOT 1	N SLOT
				0	SLOT 2	
			0	1	SLOT 3	
		0	_		SLOT 4	
		0	Į		SLOT 5	

FIG.8



PRIOR ART

FIG.9



PRIOR ART